

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Hiroyuki KANAYA *et al.*

Serial No.: TBA

Filed: June 25, 2003

For: Semiconductor Device Having Ferroelectric Capacitor and Hydrogen
Barrier Film and Manufacturing Method Thereof

Atty. Docket No.: 002372.00043

Group Art Unit: TBA

Examiner: TBA

SUBMISSION OF CORRECTED FORMAL DRAWINGS

Commissioner for Patents
P.O. Box 1460
Alexandria, VA 22313-1450

Sir:

Attached are three (3) sheets of **corrected** formal drawings (Figs. 9, 32, 52 and 53) for filing in the above application. The corrections were previously approved in patent application 09/570,026.

Respectfully submitted,

By: 

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Date: June 25, 2003

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